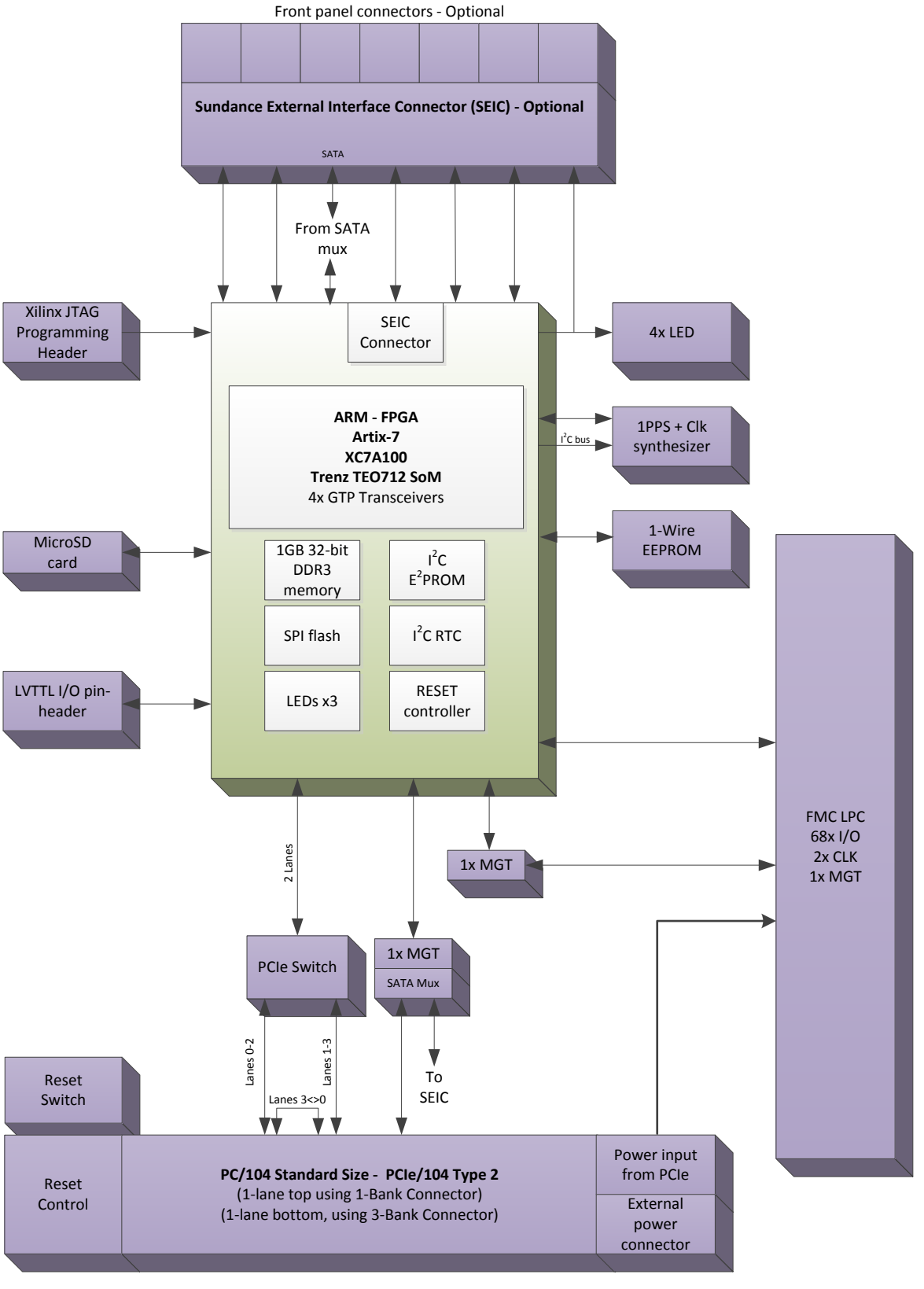
Front panel connectors - Optional
Sundance External Interface Connector (SEIC) - Optional
SATA
From SATA mux
Xilinx JTAG Programming Header
SEIC Connector
4x LED
ARM - FPGA
Artix-7
XC7A100
Trenz TEO712 SoM
4x GTP Transceivers
I²C bus
1PPS + Clk synthesizer
1-Wire EEPROM
MicroSD card
1GB 32-bit DDR3 memory
I²C E²PROM
SPI flash
I²C RTC
LVTTL I/O pin-header
LEDs x3
RESET controller
FMC LPC
68x I/O
2x CLK
1x MGT
2 Lanes
1x MGT
PCIe Switch
1x MGT
SATA Mux
Lanes 0-2
Lanes 1-3
Lanes 3<>0
To SEIC
Reset Switch
Reset Control
PC/104 Standard Size - PCIe/104 Type 2
(1-lane top using 1-Bank Connector)
(1-lane bottom, using 3-Bank Connector)
Power input from PCIe
External power connector