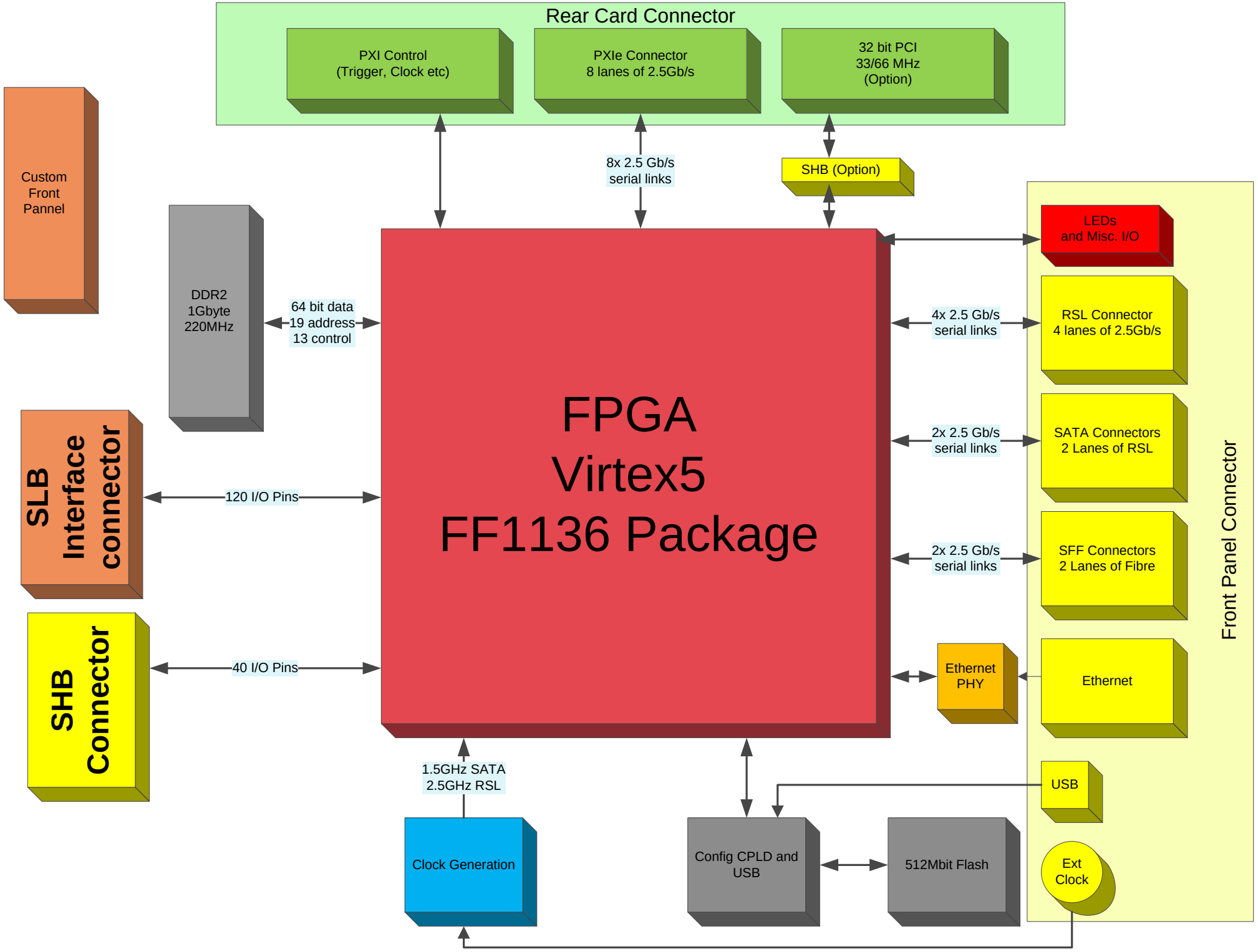
Rear Card Connector
PXI Control
(Trigger, Clock etc)
PXIe Connector
8 lanes of 2.5Gb/s
32 bit PCI
33/66 MHz
(Option)
Custom
Front
Pannel
8x 2.5 Gb/s
serial links
SHB (Option)
DDR2
1Gbyte
220MHz
64 bit data
19 address
13 control
FPGA
Virtex5
FF1136 Package
LEDs
and Misc. I/O
4x 2.5 Gb/s
serial links
RSL Connector
4 lanes of 2.5Gb/s
2x 2.5 Gb/s
serial links
SATA Connectors
2 Lanes of RSL
SLB
Interface
connector
120 I/O Pins
2x 2.5 Gb/s
serial links
SFF Connectors
2 Lanes of Fibre
Front Panel Connector
SHB
Connector
40 I/O Pins
Ethernet
PHY
Ethernet
1.5GHz SATA
2.5GHz RSL
USB
Clock Generation
Config CPLD and
USB
512Mbit Flash
Ext
Clock